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PPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
10 050,169	01 18 2002	Shinichiroh Ikemasu	970607B	9818	
23850	7590 05 28 2003				
ARMSTRONG, WESTERMAN & HATTORI, LLP 1725 K STREET, NW SUITE 1000			FXAMINER		
			WEISS, HOWARD		
WASHINGTO	ON, DC 20006		ART UNIT	PAPER NUMBER	
		2814			
			DATE MAILED: 05 28 2003	}	

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

Applicant(s)

10/050 169

Howard Weiss

Examiner

IKEMASU ET AL

Art Unit

2814

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --Period for Reply

A SHORTENED STATUTORY	PERIOD FOR REPLY IS	SET TO EXPIRE	3 MONTH(S) FROM
THE MAILING DATE OF THIS	COMMUNICATION		

- Extensions of time may be available under the provisions of 37 CFR 1 136(a). In no event, however, may a reply be timely filed
- after SIX (6) MONTHS from the mailing date of this communication
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely
 If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication

 Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U S C § 133) Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1 704(b) 						
Status	patent term aujusiment. See 37 Cl K 1704(b)					
1)[💉	Responsive to communication(s)	filed on	11 March 2003	<u>3</u> .		
2a) <u></u> □	This action is FINAL .	2b)⊠	This action is	non-fina	al.	
3)	·— · · · · · · · · · · · · · · · · · ·					
Disposition	closed in accordance with the pra on of Claims	ictice un	der Ex parte Q	uayle, 1	935 C.D. 11. 453	3 O.G. 213.
4) <u> </u>	4)⊡ Claim(s) <u>1-108</u> is/are pending in the application.					
4	4a) Of the above claim(s) is/are withdrawn from consideration.					
5) 🗌 (5) Claim(s) is/are allowed.					
6)[] (6) ☑ Claim(s) 1-108 is/are rejected.					
7) 🗌 (7) Claim(s) is/are objected to.					
8) 🗌 (8) Claim(s) are subject to restriction and/or election requirement.					
Applicatio	n Papers					
9)[] T	he specification is objected to by t	he Exan	niner.			
10) T	he drawing(s) filed on is/ard	e: a) 🗌 a	ccepted or b)	objected	to by the Examir	ner.
	Applicant may not request that any c	bjection t	o the drawing(s)	be held i	in abeyance See	37 CFR 1 85(a)
11) 🗌 T	he proposed drawing correction fil	ed on _	is: a)□ a	pproved	b) disapprove	d by the Examiner.
If approved, corrected drawings are required in reply to this Office action						
12) 🗌 TI	he oath or declaration is objected	to by the	e Examiner.			
Priority ur	nder 35 U.S.C. §§ 119 and 120					
13) 🗌 🛮 A	Acknowledgment is made of a clai	m for for	eign priority un	ider 35 L	J.S.C. § 119(a)-(d) or (f).
a) ☐ All b) ☐ Some * c) ☐ None of:						
1	1. Certified copies of the priority documents have been received.					
2	2. Certified copies of the priority documents have been received in Application No					
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 						
14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).						
	_				-	• • • • • • • • • • • • • • • • • • • •
 a) ☐ The translation of the foreign language provisional application has been received. 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121. 						
Attachment(s	•		•		- 3	
	of References Cited (PTO-892)			4, 🔲 In	iterview Summary (P	TO-413) Paper No(s)
2) Notice	of Draftsperson's Patent Drawing Review	PTO-948;	•	5) 🔲 N	otice of Informal Pate	ent Application (PTO-152)

3) Information Disclosure Statement(s) (PTO-1449) Paper No(s)

Other

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Attorney's Docket Number: 970607B

Filing Date: 1/18/02

Continuing Data: Division of 08/876,908 (6/16/97 now U.S. Patent No. 6,344,692)

Claimed Foreign Priority Date: 7/18/96 (JPX)

Applicant(s): Ikemasu et al. (Okawa)

Examiner: Howard Weiss

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

2. Claims 1 to 3, 8 to 10, 12, 13, 17, 24 to 26, 31 to 33, 38 to 40, 50 to 52, 56 to 60, 64, 65 and 72 are rejected under 35 U.S.C. § 102(e) as being anticipated by Auer et al. (U.S. Patent No. 5,623,164).

Auer et al. show all aspects of the instant invention (e.g. Figure 1) including:

- a substrate 1
- a switching or memory cell transistor with an insulated gate 6 and source/drain regions 3,4

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an insulator laminate including a lower part 15 covering said switching transistor and a higher part 50 having a planarized upper surface over the memory cell transistor

- a first conductive plug 9 formed in a first contact hole and connected to a source/drain region 3
- a memory cell capacitor with a storage electrode 11, a capacitor dielectric 47 and an opposing electrode 16 having an extension with is connected to a second conductive plug (not shown) via a second contact hole 18
- a third conductive plug 8 formed above said lower part and connected to an upper surface of a lower conductive member 4
- contact holes and conductive members (i.e. bit lines) as claimed in a peripheral circuit

Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. Claims 4 to 7. 11, 14 to 16, 18 to 23. 27 to 30, 34 to 37, 41 to 49, 53 to 55, 61 to 63, 66 to 71 and 73 to 108 are rejected under 35 U.S.C. 103(a) as being unpatentable over Auer et al. and Ozaki et al. (U.S. Patent No. 5,414,655).

Auer et al. show most aspects of the instant invention (Paragraph 2) except for the cylinder shape of the storage electrode and for the nitride film covering the claimed features in the memory device. Ozaki et al. teach (e.g. Figures 1 to 22) to make storage electrodes 25 cylindrical in shape and to cover features in a DARM with silicon nitride layers 13,22 to permit easy connection to the substrate while

maintaining a large storage capacity (Column 1 Line 66 to Column 2 Line 2). It would have been obvious to a person of ordinary skill in the art at the time of invention to make storage electrodes cylindrical in shape and to cover features in a DARM with silicon nitride layers as taught by Ozaki et al. in the device of Auer et al. to permit easy connection to the substrate while maintaining a large storage capacity.

Conclusion

- 5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Takaishi (U.S. Patent No. 5,689,126) show a DRAM similar to the instant invention.
- 6. Papers related to this application may be submitted directly to Art Unit 2814 by facsimile transmission. Papers should be faxed to Art Unit 2814 via the Art Unit 2814 Fax Center located in Crystal Plaza 4, room 3C23. The faxing of such papers must conform with the notice published in the Official Gazette, 1096 OG 30 (15 November 1989). The Art Unit 2814 Fax Center number is (703) 308-7722 or -7724. The Art Unit 2814 Fax Center is to be used only for papers related to Art Unit 2814 applications. The official TC2800 Before-Final, (703) 872-9318, and After-Final, (703) 872-9319. Fax numbers will provide the fax sender with an auto-reply fax verifying receipt of their fax by the USPTO.
- 7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Howard Weiss at **(703)** 308-4840 and between the hours of 8:00 AM to 4:00 PM (Eastern Standard Time) Monday through Friday or by e-mail via Howard.Weiss@uspto.gov.

Any inquiry of a general nature or relating to the status of this application should be directed to the Group 2800 Receptionist at (703) 308-0956.

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8. The following list is the Examiner's field of search for the present Office Action:

Field of Search	Date
U.S. Class / Subclass(es): 257/ 306, 311, 535	thru 5/19/03
Other Documentation: none	
Electronic Database(s): EAST	thru 5/19/03

HW/hw 20 May 2003 Howard Weiss Examiner Art Unit 2814